

INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION

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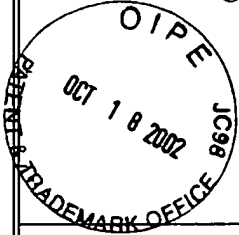
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APPLICANT
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February 28, 2000

GROUP
2749

(PTO-1449)



U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
TL	0 792 077	08/27/1997	European				
TL	WO 95/04991	02/16/1995	International				
TL	2 298 109	08/21/1996	Great Britain				
TL	WO 98/29816	07/09/1998	International				
TL	1 046 980	10/25/2000	European				
TL	0 498 454	08/12/1992	European				
TL	WO 00/46668	08/10/2000	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

TL	"BROWN ET AL: 'Simultaneous code execution and data storage in a single flash memory chip for real time wireless communication systems" CIRCUITS AND SYSTEMS, 1997. PROCEEDINGS OF THE 40TH MIDWEST SYMPOSIUM ON SACRAMENTO, CA, USA, 3-6 AUG. 1997, NEW YEAR, IEEE, 3 August 1997 (1997-08-03), pages 740-745, IP010272311 ISBN: 0-7803-3694-1 *the whole document*

EXAMINER

DATE CONSIDERED

3/12/03

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.